SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institution with permanent Affiliation to JNTUK, Kakinada) Seshadri Rao Knowledge Village, Gudlavalleru – 521 356, Krishna Dist., A.P.

Examination Section

II B.Tech - I Semester (R20) Regular Examinations, November 2025 (Readmitted Students)

TIME TABLE

4th November 2025

Timings: 01.30 pm to 04.30 pm

71.50 pm to 04.50 pm		
DATE & DAY	ECE	CSE
17.11.2025 (Monday)	Probability Theory and Stochastic Processes (EC3508)	Managerial Economics and Financial Analysis (BA3502)
19.11.2025 (Wednesday)	Semiconductor Devices and Circuits (EC3509)	Digital Logic Design (CT3510)
21.11.2025 (Friday)	Signals and Systems (EC3510)	Object Oriented Programming through Java (CT3511)
25.11.2025 (Tuesday)	Electromagnetic Field Theory (EC3511)	Probability and Statistics (MA3509)
27.11.2025 (Thursday)	Digital Circuit Design (EC3512)	Database Management Systems (CT3512)
29.11.2025 (Saturday)	Numerical Methods and Complex Analysis (MA3508) Timings: 10.00 am to 01.00 pm	- <u>-</u> -

Note: Seating arrangement will be notified on the day of each examination half an hour before the commencement of the examination.

Controller of Examinations

Copy to: HoDs -ECE/CSE/CoE

Tables of Principal/Vice-Principal (Academics)/Vice Principal (Administration)